

UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

CARL M. ELLISON

Application No.: 09/540,613

Filed: March 31, 2000

For: MANAGING A SECURE

ENVIRONMENT USING A CHIPSET IN

ISOLATED EXECUTION MODE

Art Group: 2782

Examiner: Unassigned

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INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

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In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s). Copies of the references cited on PTO/SB/08 are enclosed herewith.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: January 26, 2004

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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Tu T. Nguyen Date

PTO/SB/08A(10-01)
Approved for use through 10/31/2002, 00MB 651-0031
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no persons are required to respond to a collection of information unless it contains a valid OMB control number. Substitute for form 1449A/PTO INFORMATION DISCLOSURE 09/540613 **Application Number** STATEMENT BY APPLICANT (Use as many sheets as necessary) March 31, 2000 **Filing Date** Ellison, Carl **First Named Inventor** Unknown **Group Art Unit** Unknown **Examiner Name** Attorney Docket No: 42P08628

FOREIGN PATENT DOCUMENTS									
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T²			
	DE-DE4217444	12/03/1992	Toyohisa, Imada, et al.		051	/			
	EP-EP0473913	03/11/1992	Farrell, Joel A.	HE	FCET	VEI			
	JP-JP2000076139	03/14/2000	Tanno, Masaaki , et al.						
	WO-WO0175564	10/11/2002	Herbert, Howard C., et al.	ľ	WAY 1 3	2004			
	WO-WO02086684	10/31/2002	Proudler, Graeme J.						
	WO-WO0217555	02/28/2002	Ford, Warwick	Techr	ology Ce	nter 2			

	OTHE	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		BRANDS, STEFAN, "Restrictive Blinding of Secret-Key Certificates",	
		<u>SPRINGER-VERLAG XP002201306,</u> (1995),Chapter 3	ļ.,,,
		CHIEN, ANDREW A., et al., "Safe and Protected Execution for the Morph/AMRM	
		Reconfigurable Processor", 7th Annual IEEE Symposium, FCCM '99	
		Proceedings, XP010359180, ISBN 0-7695-0375-6, Los Alamitos, CA,	1
		(4/21/1999),209-221	
		COMPAQ COMPUTER CORPORATION, et al., "Trusted Computing Platform	
		Alliance (TCPA) Main Specification Version 1.1a", (12/2001),1-321	<u> </u>
		DAVIDA, GEORGE I., et al., "Defending Systems Against Viruses through	
		Cryptographic Authentication", Proceedings of the Symposium on Security and	
		Privacy, IEEE Comp. Soc. Press, ISBN 0-8186-1939-2,(May 1989),	
		IBM, "Information Display Technique for a Terminate Stay Resident Program	
		IBM Technical Disclosure Bulletin", <u>TDB-ACC-No. NA9112156</u> , Vol. 34, Issue	
		<u>7A,</u> (12/1/1991),156-158	L
		KARGER, PAUL A., et al., "A VMM Security Kernal for the VAX Architecture",	
		Proceedings of the Symposium on Research in Security and Privacy,	
		XP010020182, ISBN 0-8186-2060-9, Boxborough, MA, (5/7/1990),2-19	_
		KASHIWAGI, KAZUHIKO, et al., "Design and Implementation of Dynamically	ľ
		Reconstructing System Software", Software Engineering Conference,	Ì
		Proceedings 1996 Asia-Pacific Seoul, South Korea 4-7 Dec. 1996, Los Alamitos,	
		CA USA, IEEE Comput. Soc, US, ISBN 0-8186-7638-8,(1996),	
		LUKE, JAHN, et al., "Replacement Strategy for Aging Avionics Computers",	
		IEEE AES Systems Magazine, XP002190614,(March 1999),	<u> </u>
		MENEZES, OORSCHOT, "Handbook of Applied Cryptography", CRC Press	
		LLC, USA XP002201307, (1997),475	
		RICHT, STEFAN, et al., "In-Circuit-Emulator Wird Echtzeittauglich", Elektronic,	
		Franzis Verlag GMBH, Munchen, DE, Vol. 40, No. 16, XP000259620,(100-	
		103),8-6-1991	<u> </u>
		ROBIN, JOHN S., et al., "Analysis of the Pentium's Ability to Support a Secure	
		Virtual Machine Monitor", Proceedings of the 9th USENIX Security Symposium,	
		XP002247347, Denver, Colorado, (8/14/00),1-17	L

EXAMINER

DATE CONSIDERED

PTO/SB/08A(10-01 Approved for use through 10/31/2002, OMB 651-0031 S Patent & Trademark Office; U.S. DEPARTMENT OF COMMERCE

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	OTHE	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		SAEZ, SERGIO, et al., "A Hardware Scheduler for Complex Real-Time	
		Systems", Proceedings of the IEEE International Symposium on Industrial	
		Electronics, XP002190615,(July 1999),43-48	
		SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture",	1
		Department of Computer Science and Engineering, University of California, San	
		Diego, La Jolla, CA, (Nov. 2001),	

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MAY 1 3 2004

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